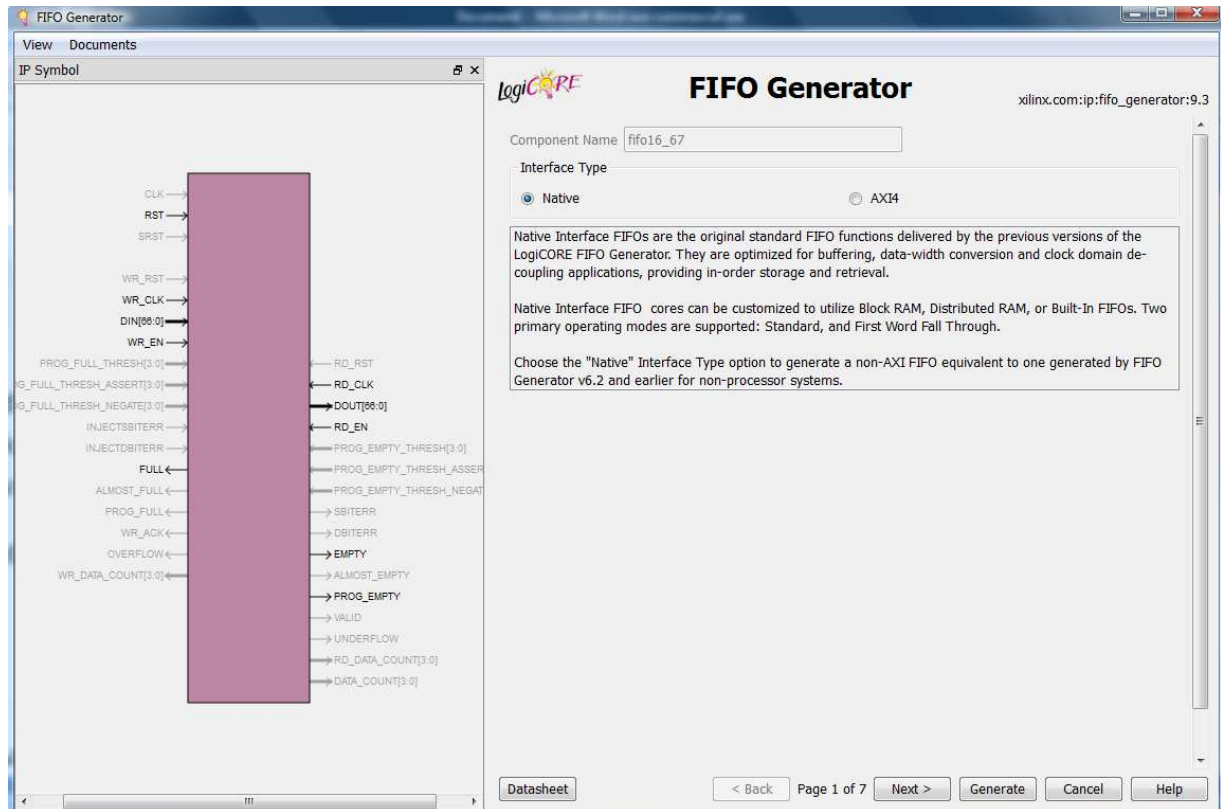
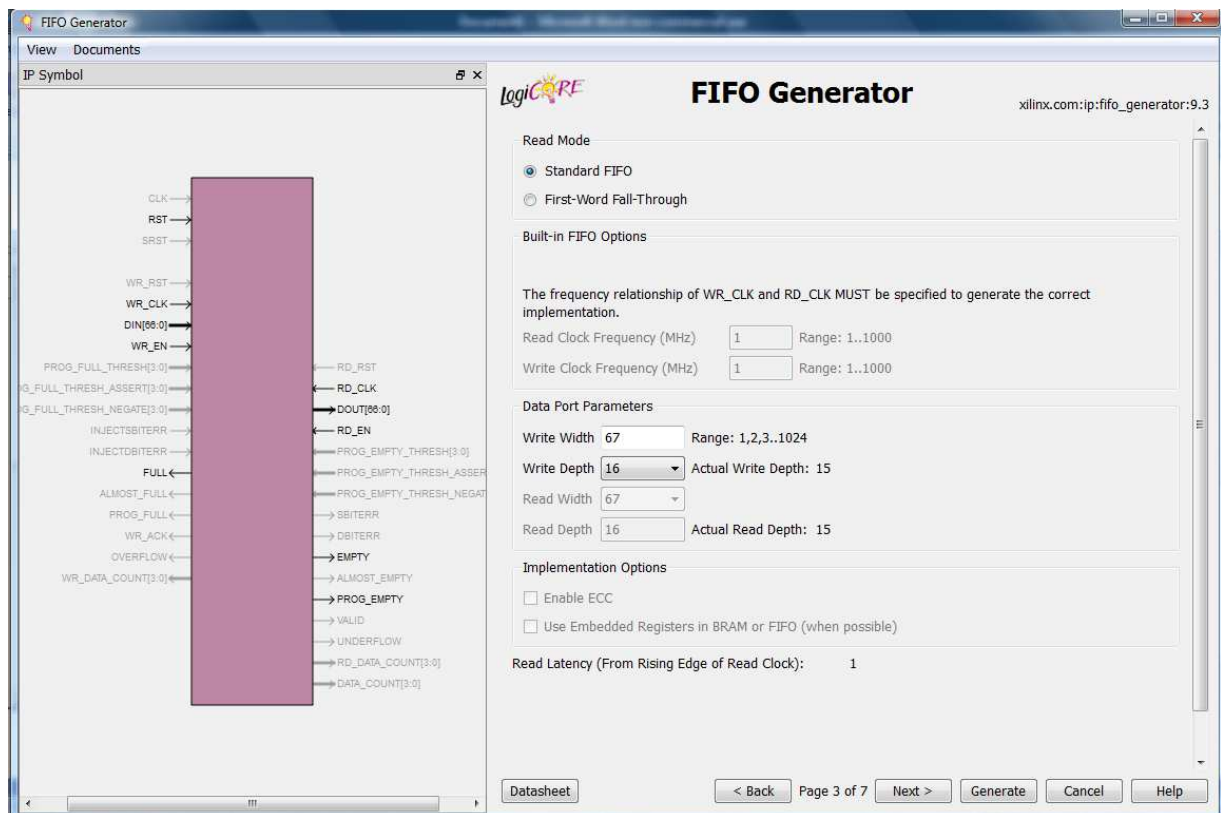
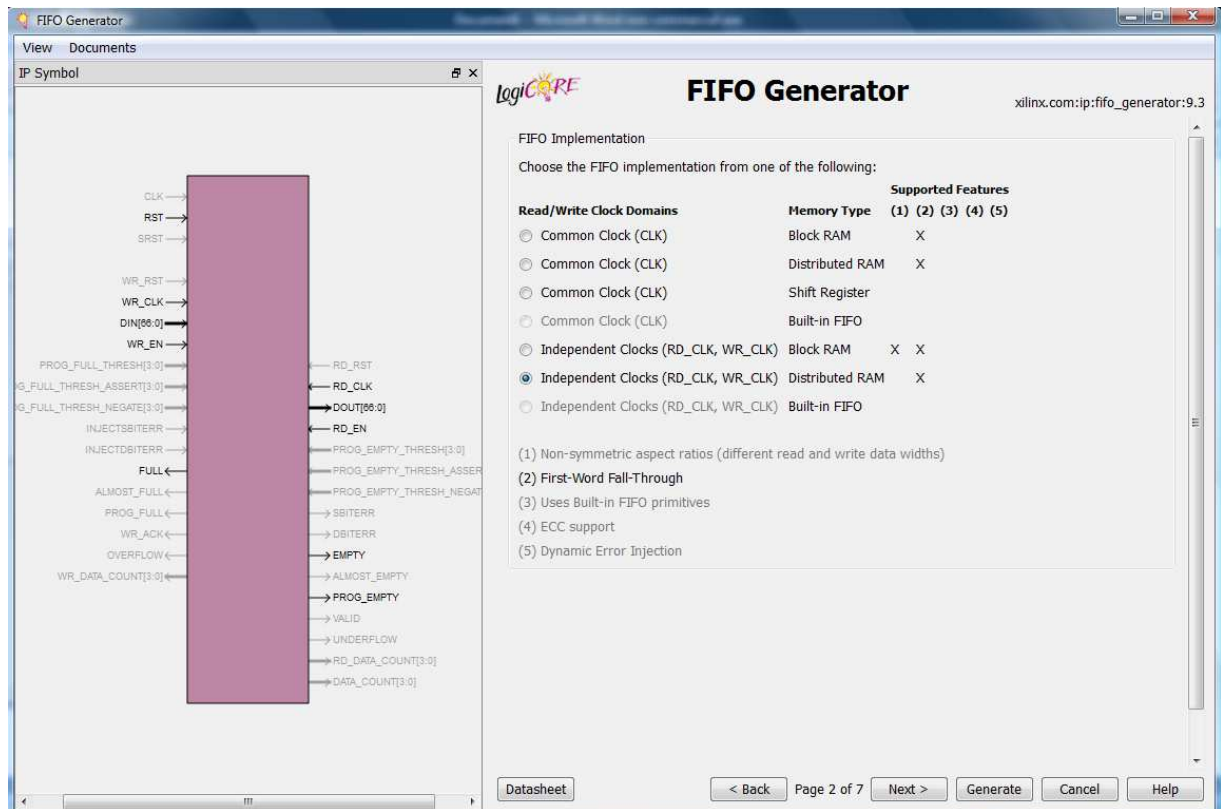
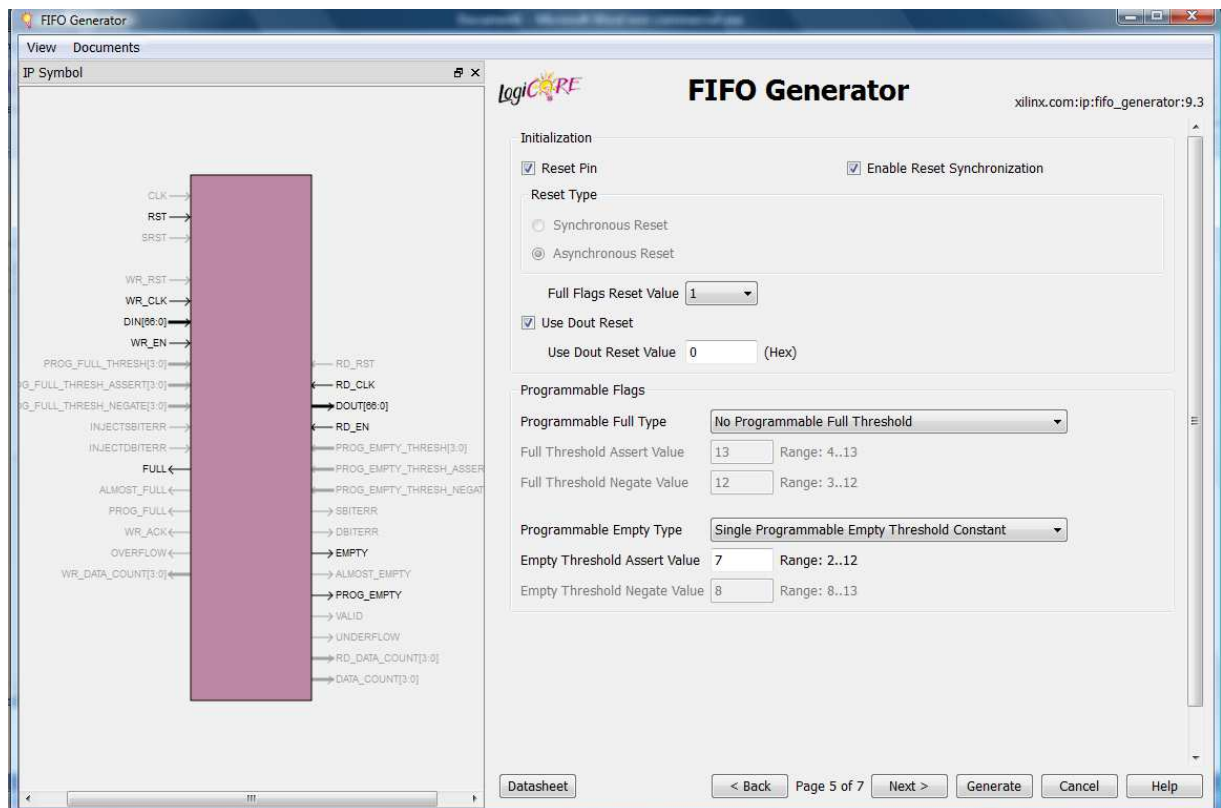
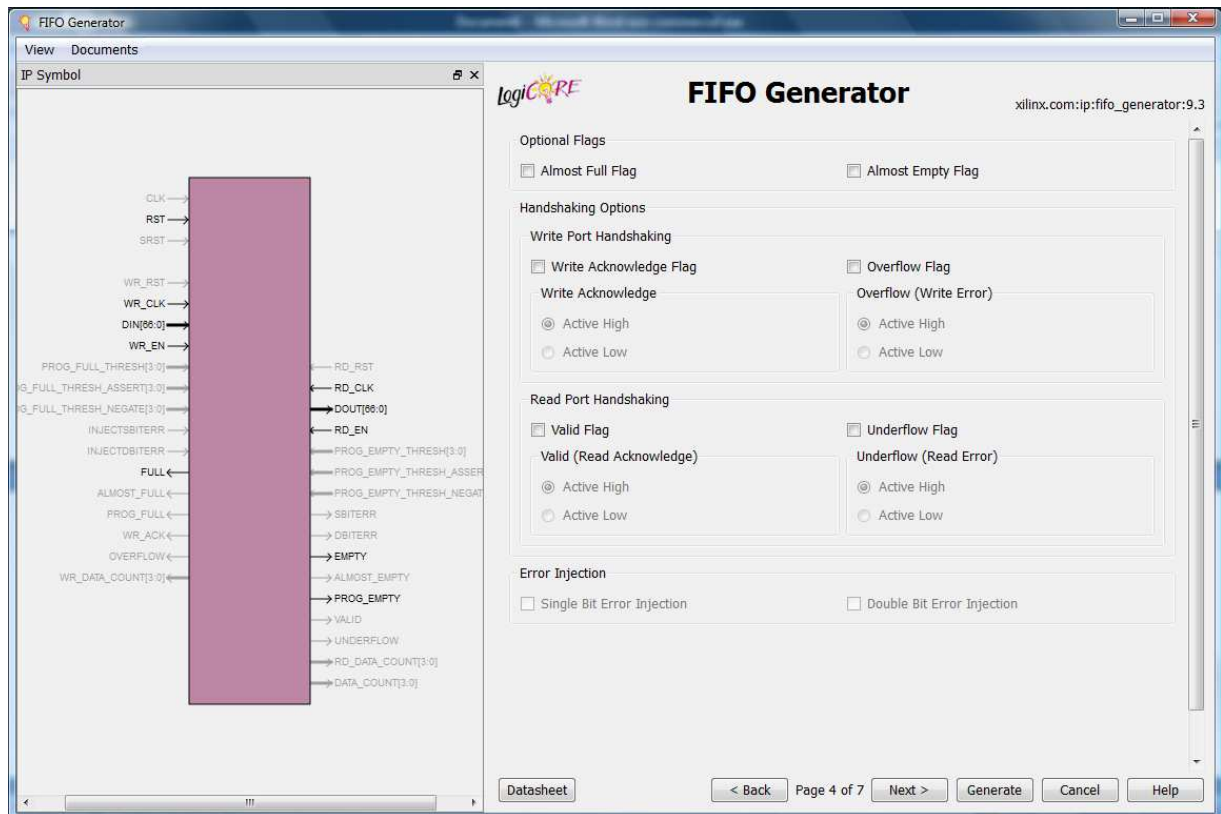


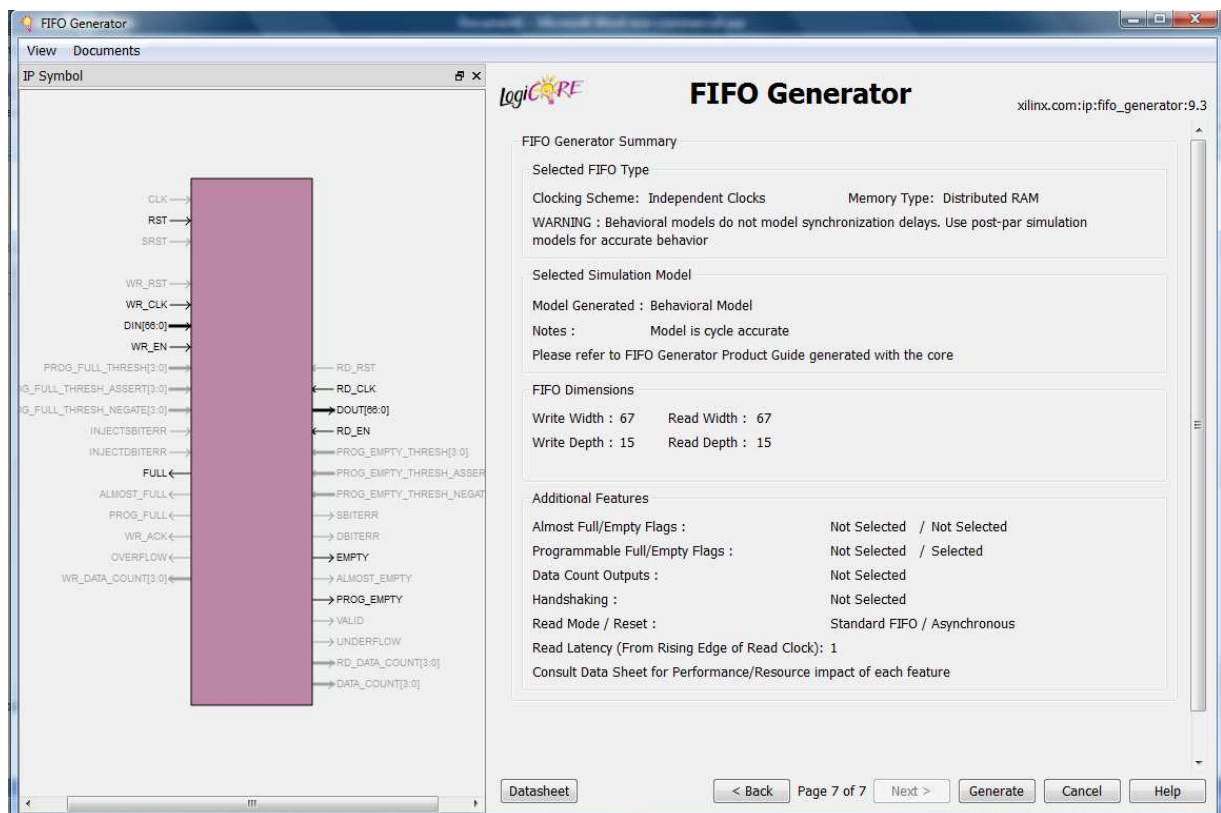
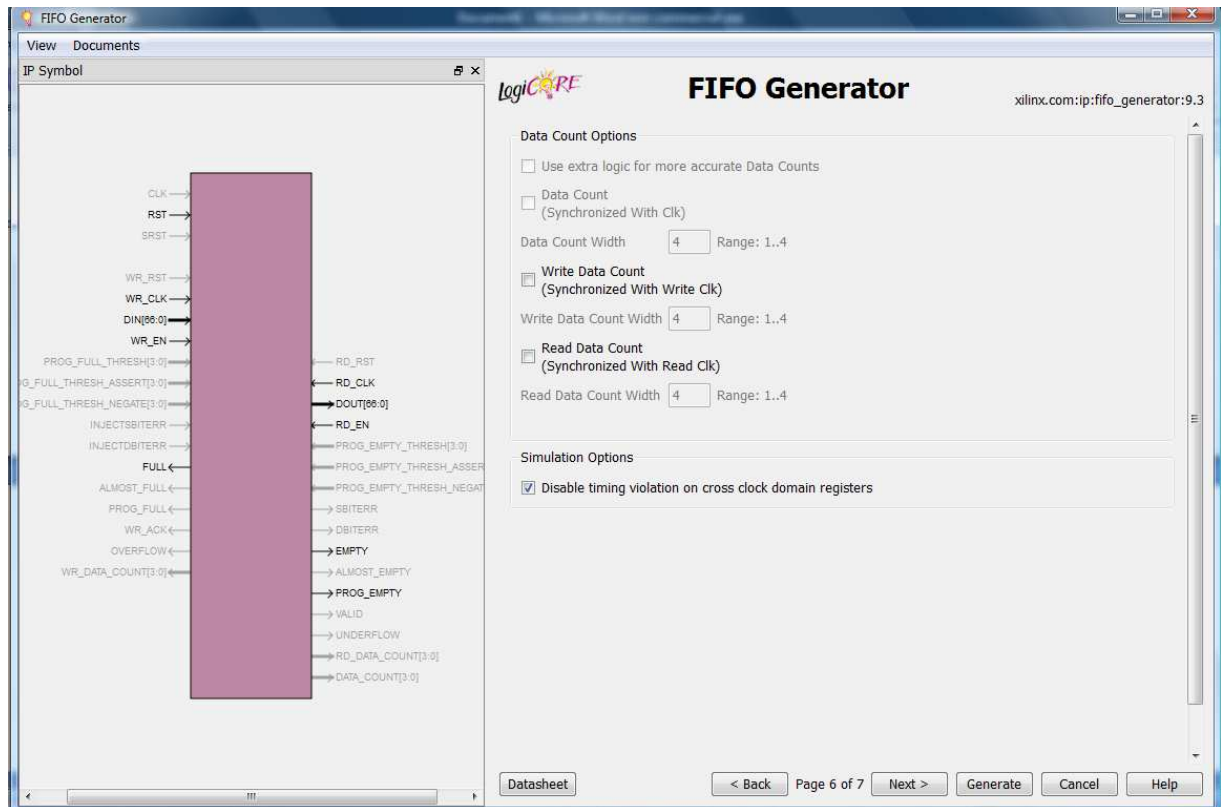
The digital part of the valve DAC uses FIR filters and FIFOs generated with the Xilinx PlanAhead software version 14.6 (which incorporates FIR compiler 5.0 and FIFO generator 9.3). The screen shots below show the used settings:

fifo16_67 used in prefilter.v

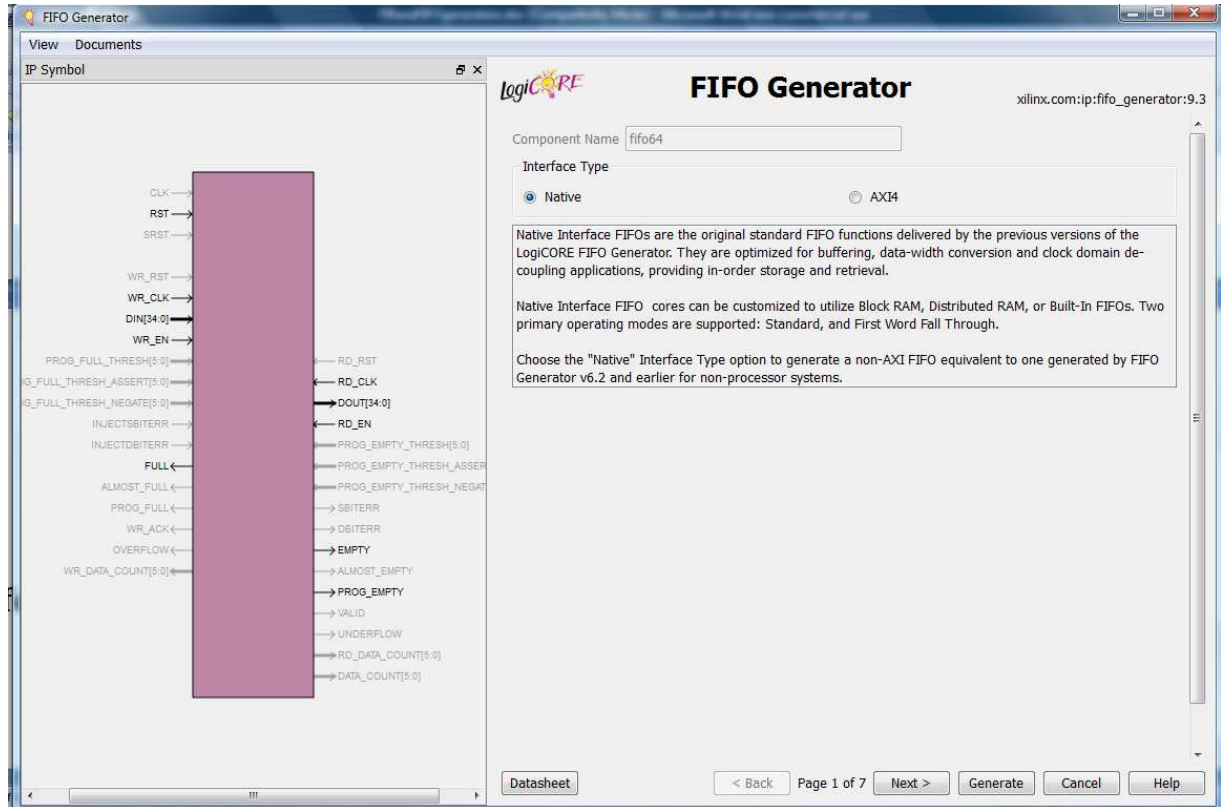


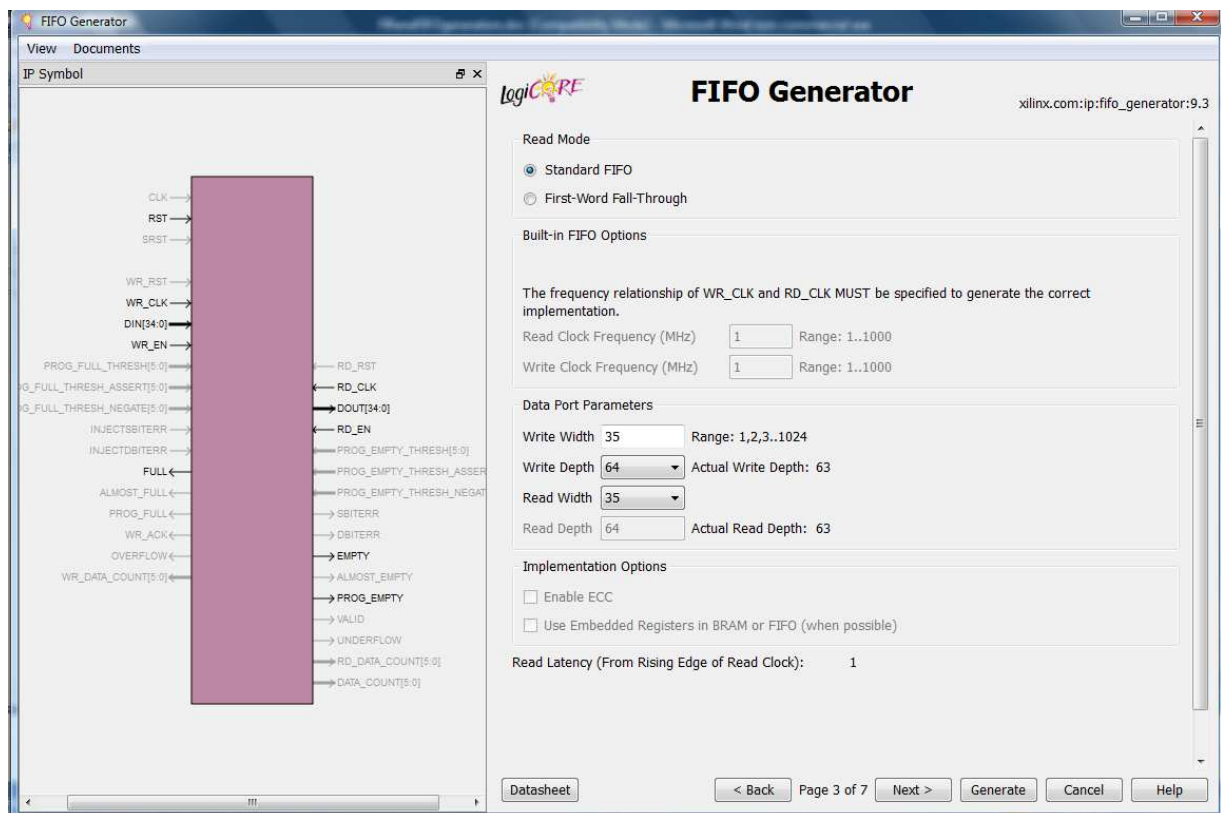
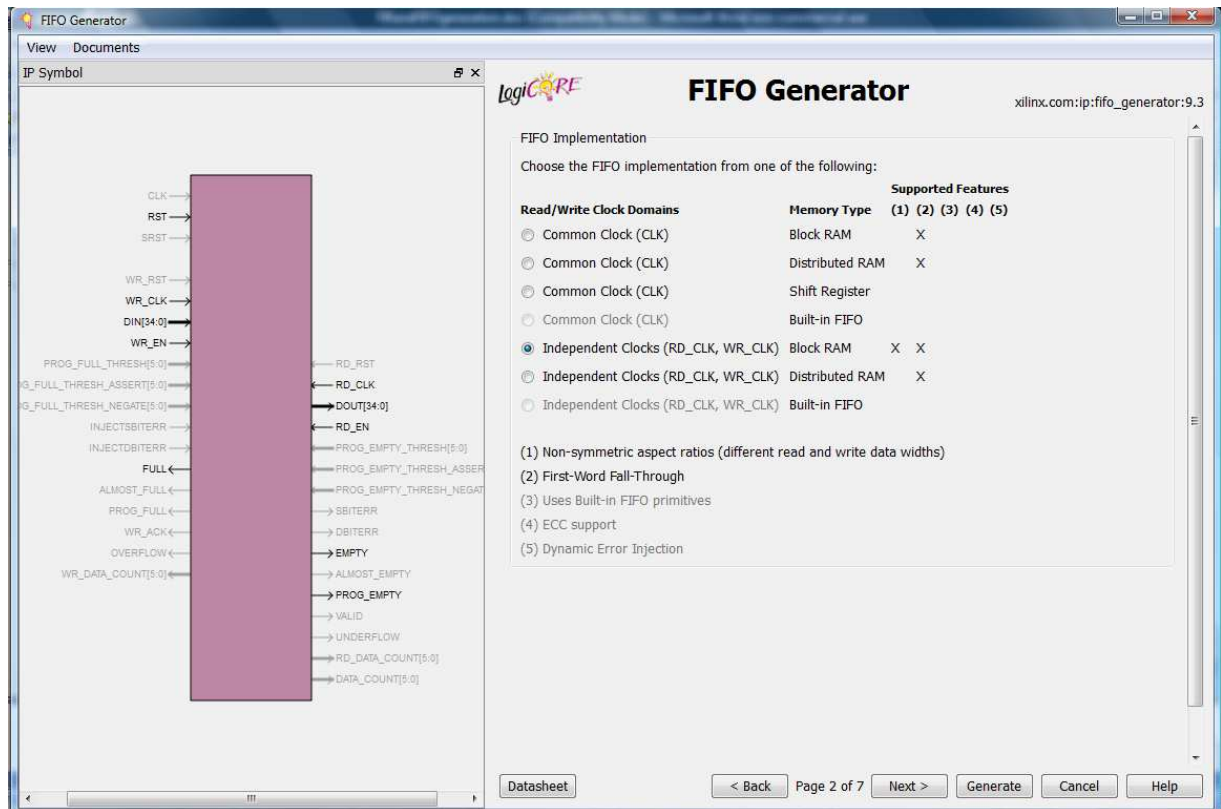


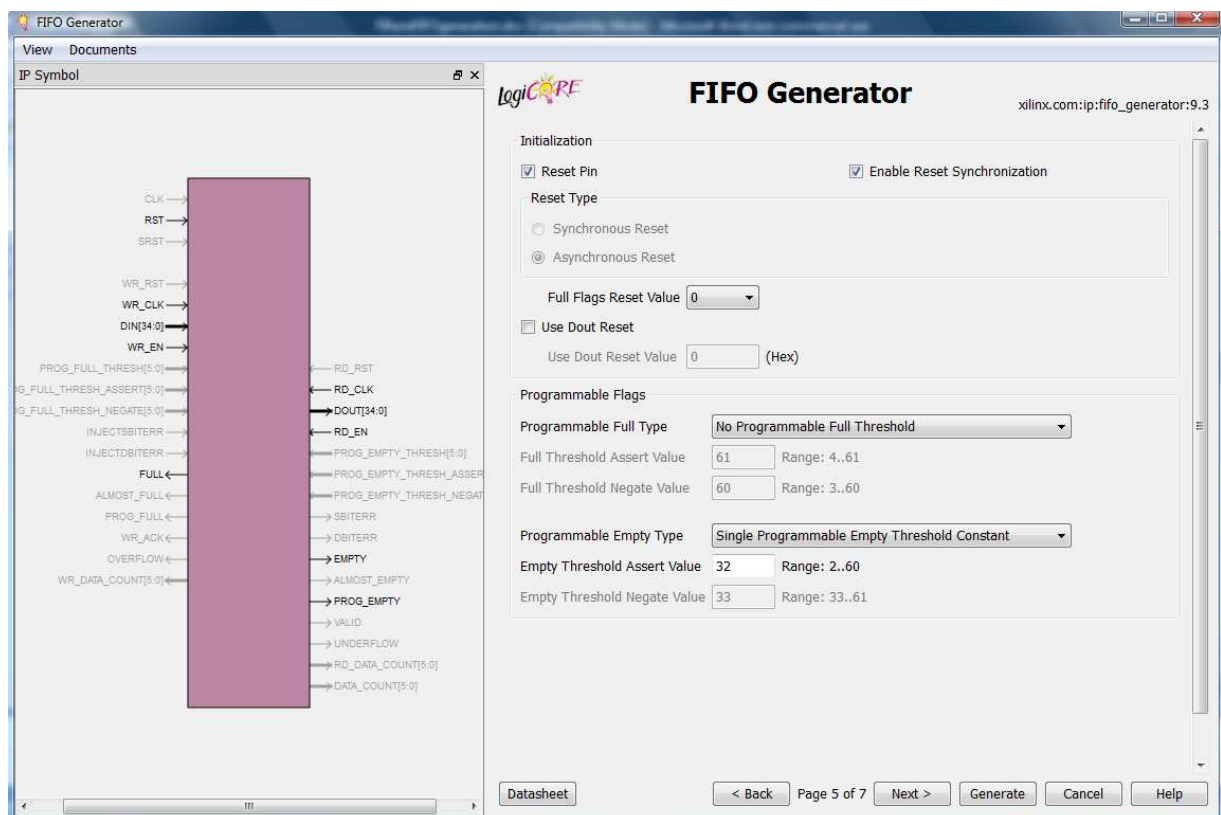
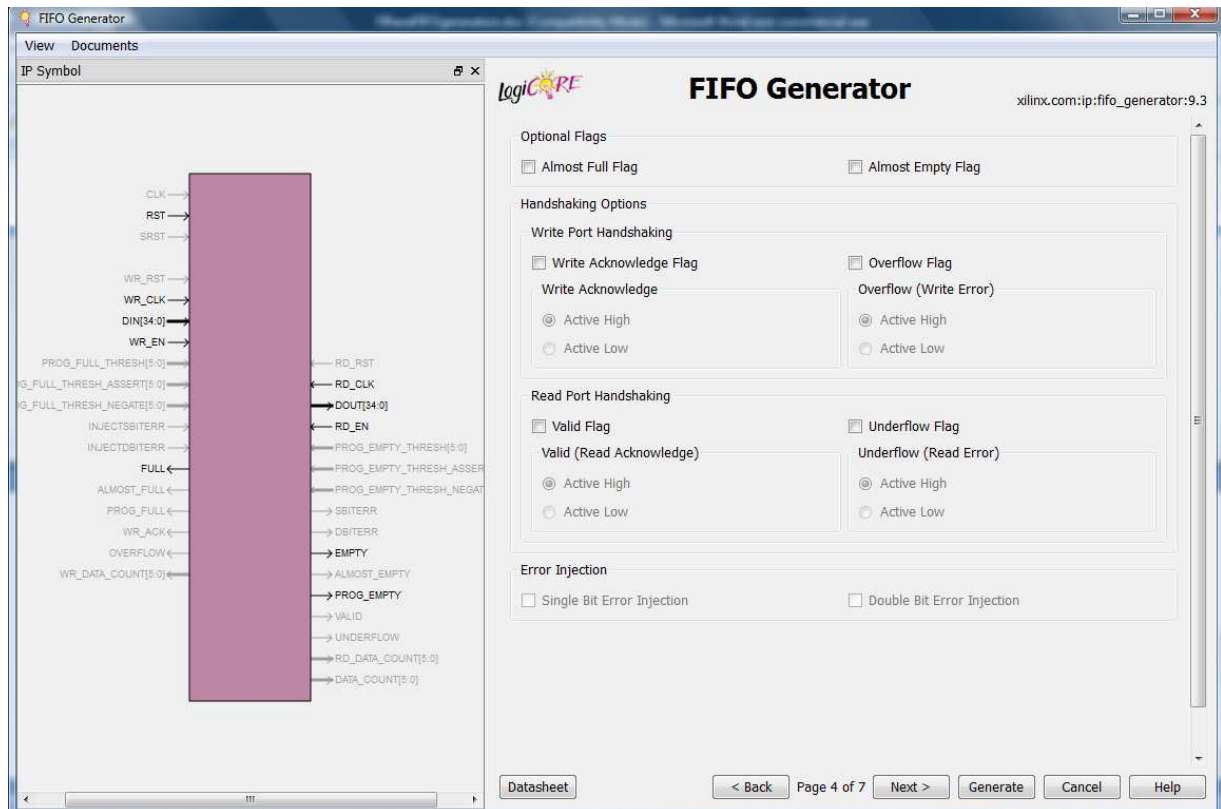


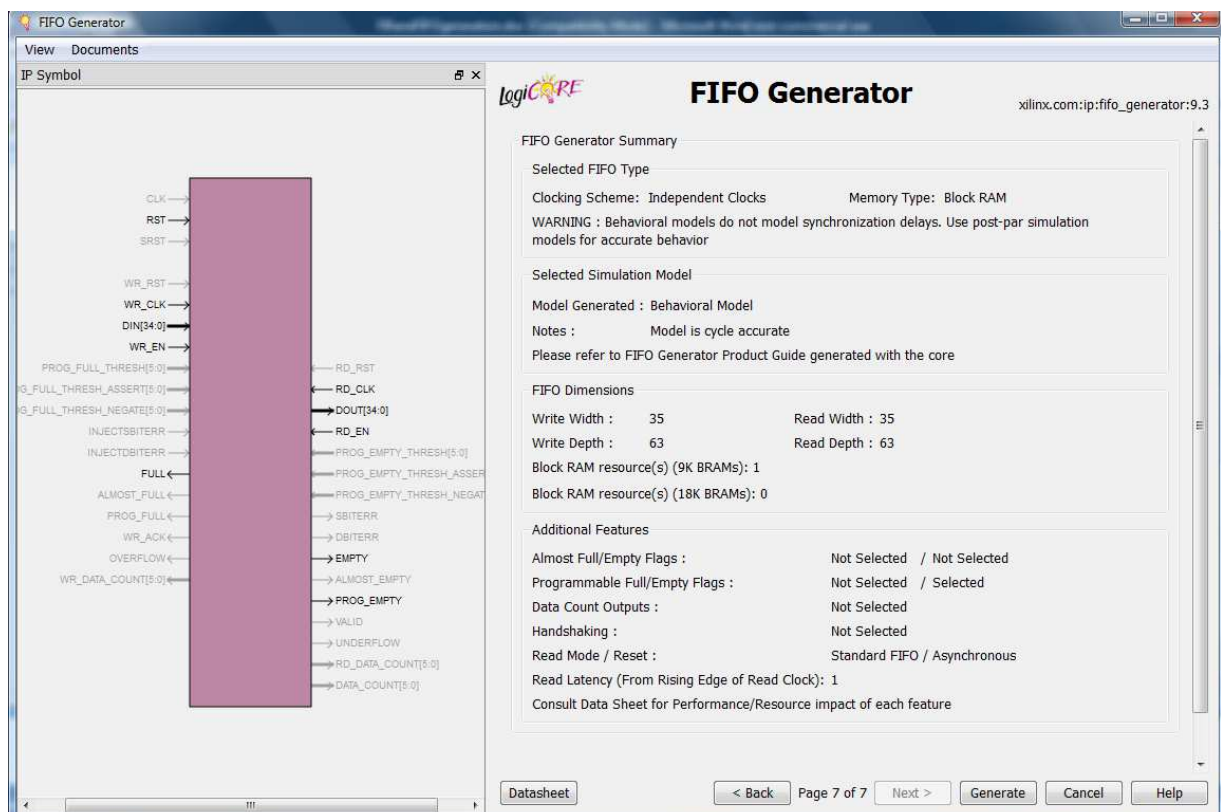
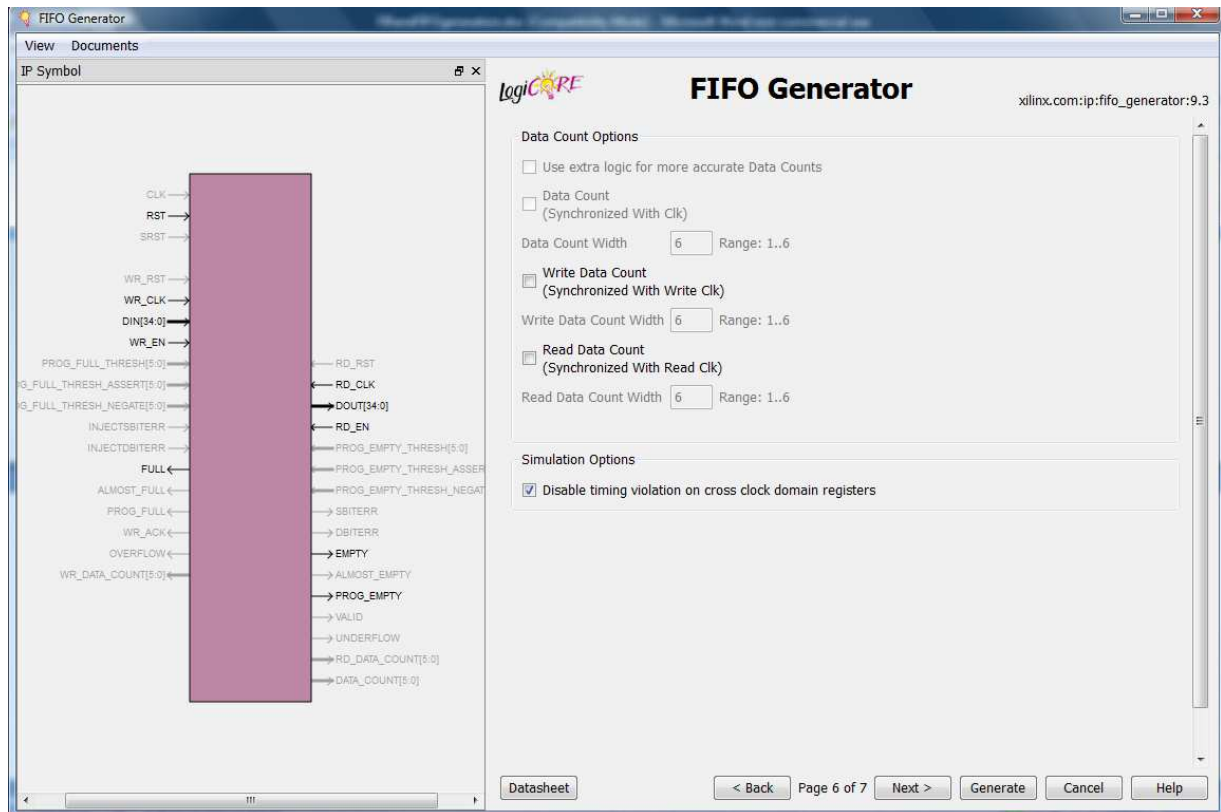


fifo64, used in interpol.v



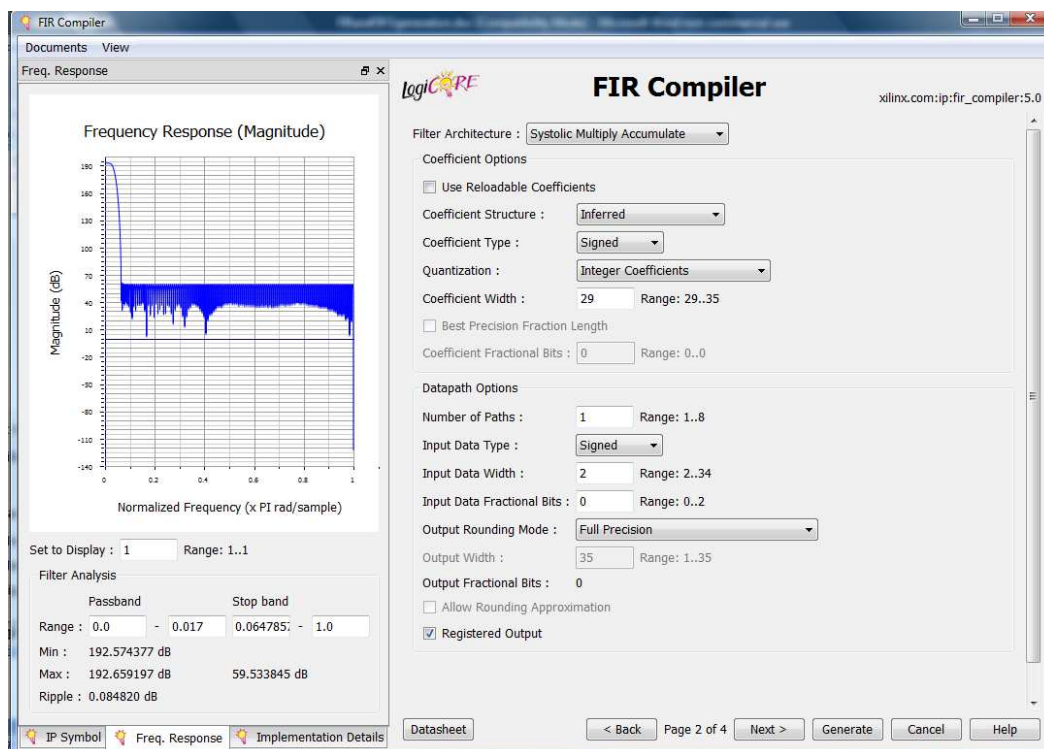
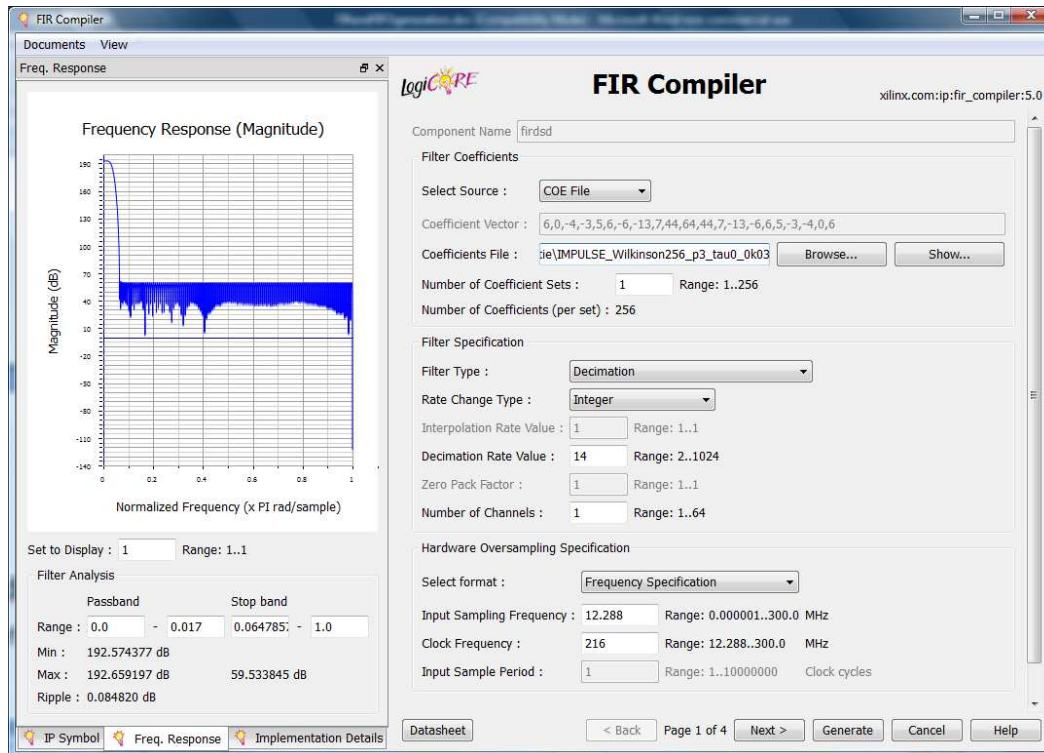


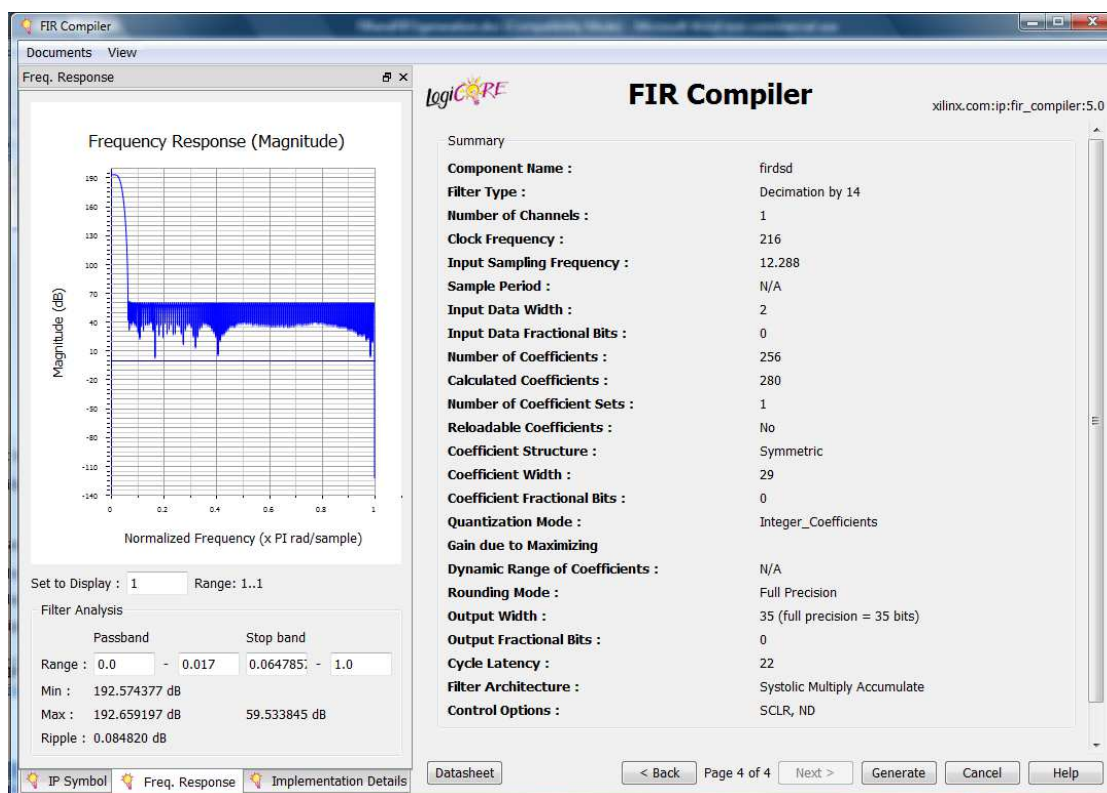
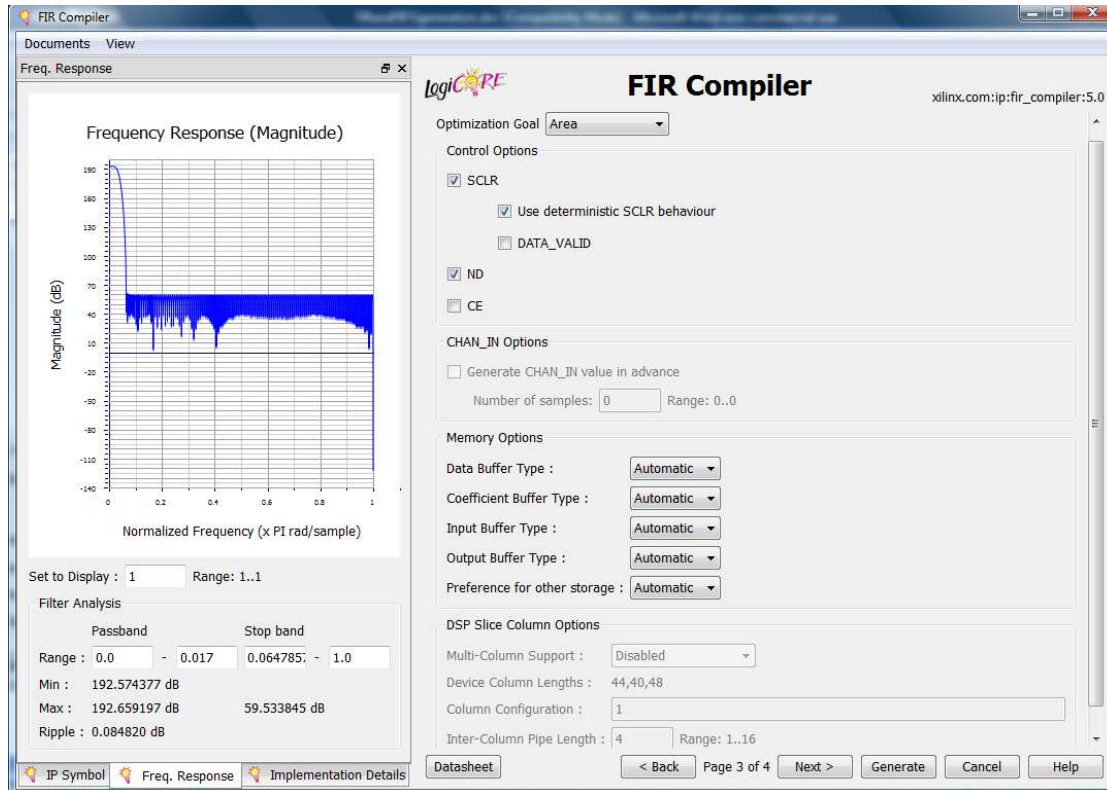




firdsd, used in prefilter.v

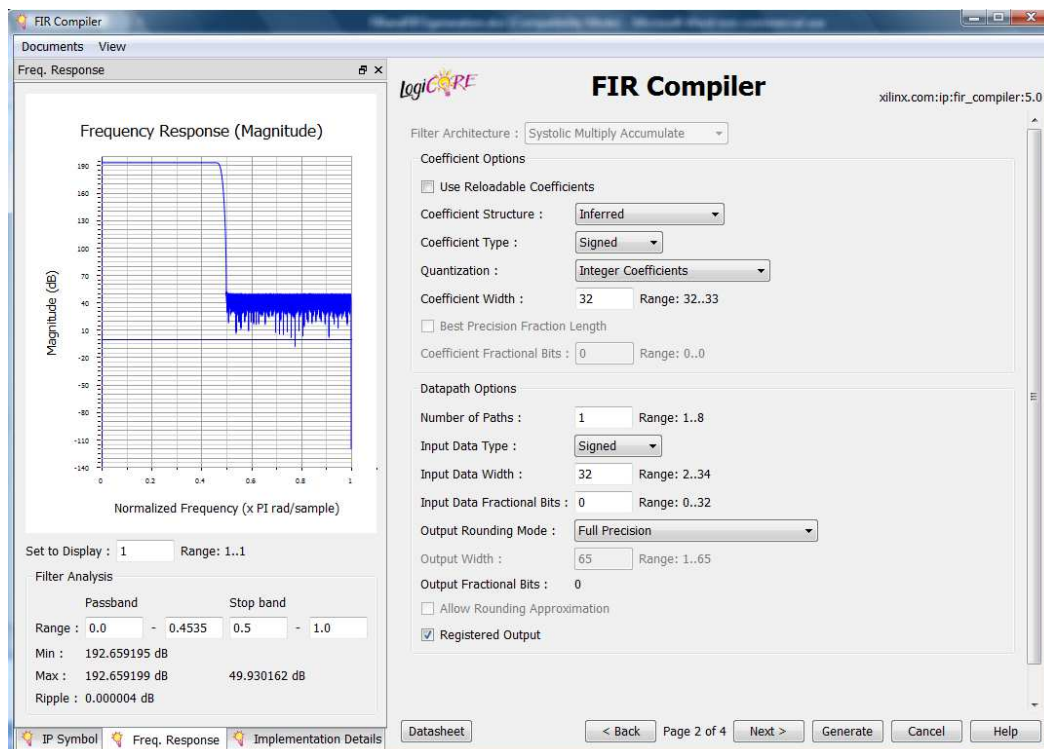
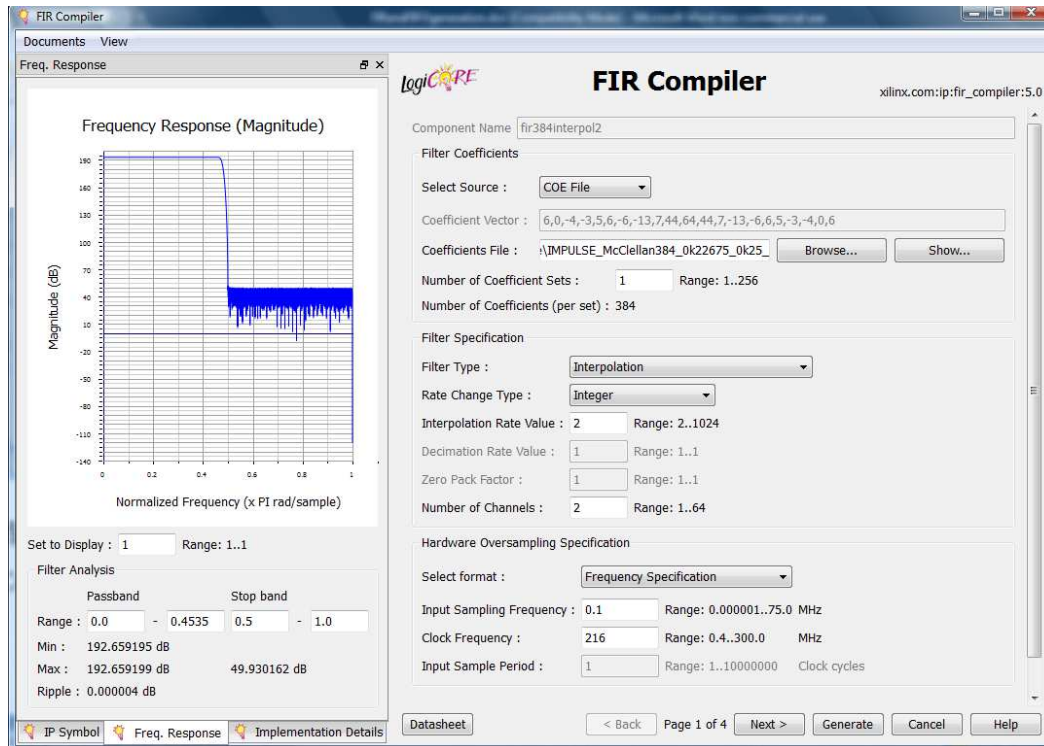
Coefficient file: IMPULSE_Wilkinson256_p3_tau0_0k03239285714.coe

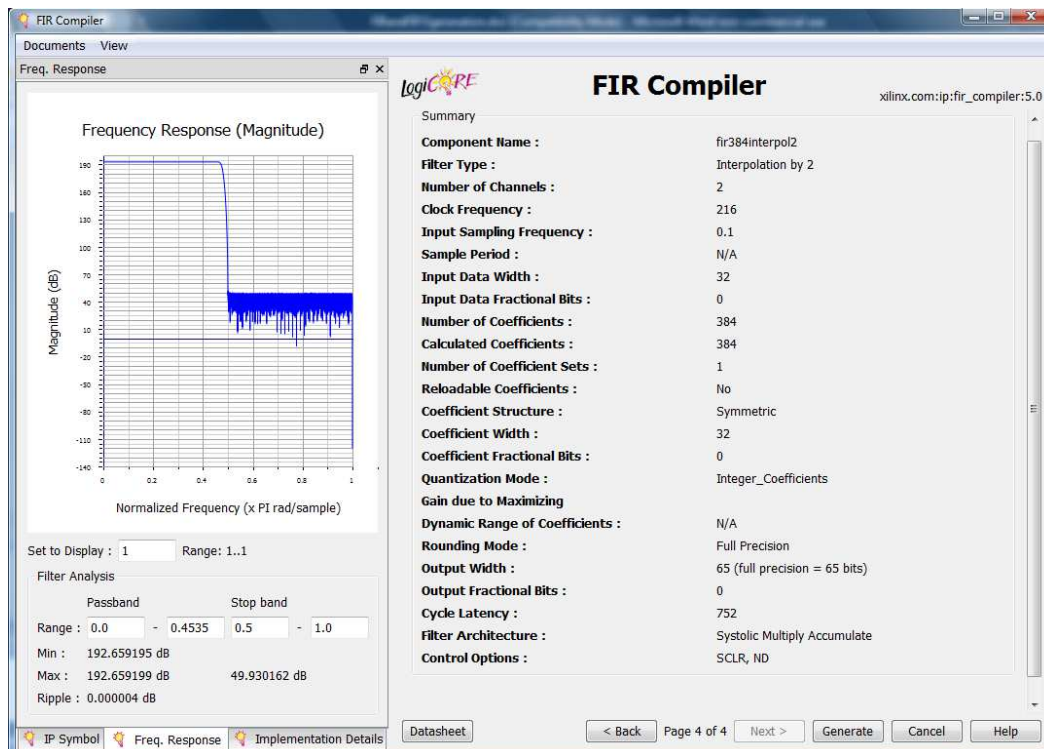
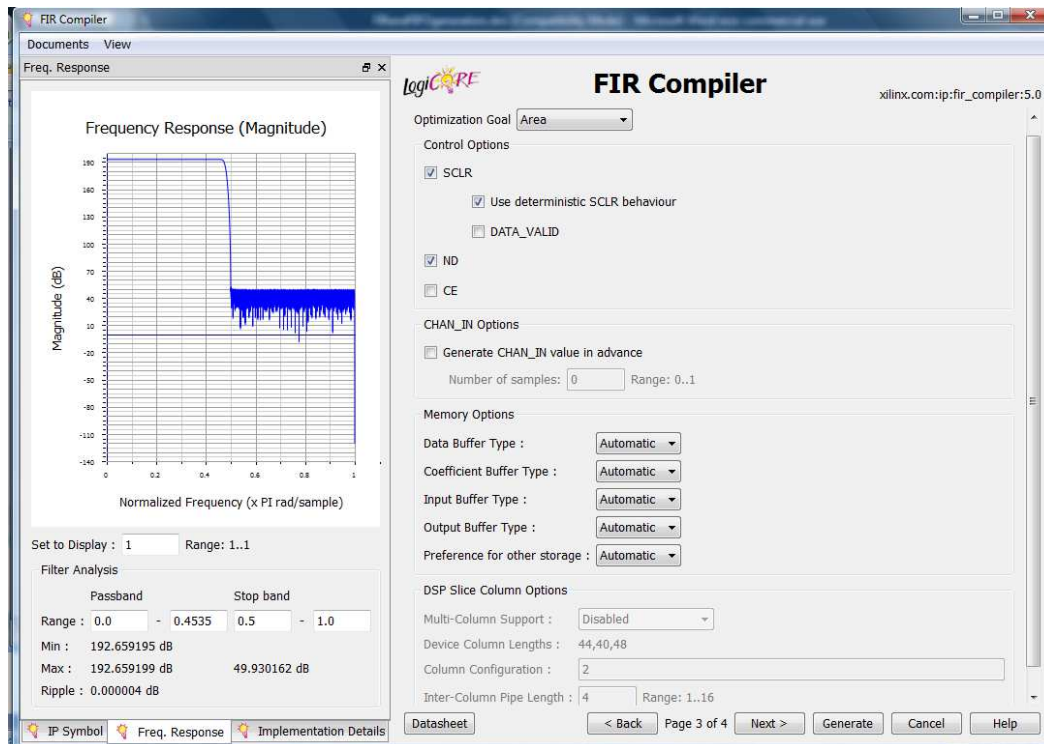




fir384interp2, used in prefilter.v

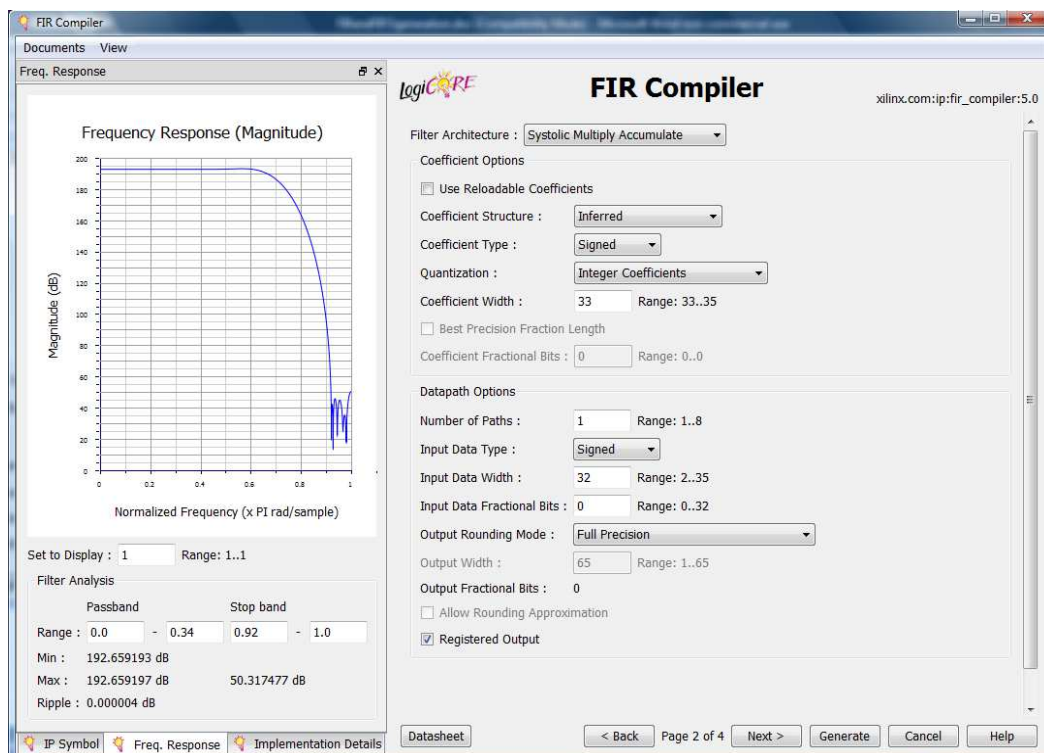
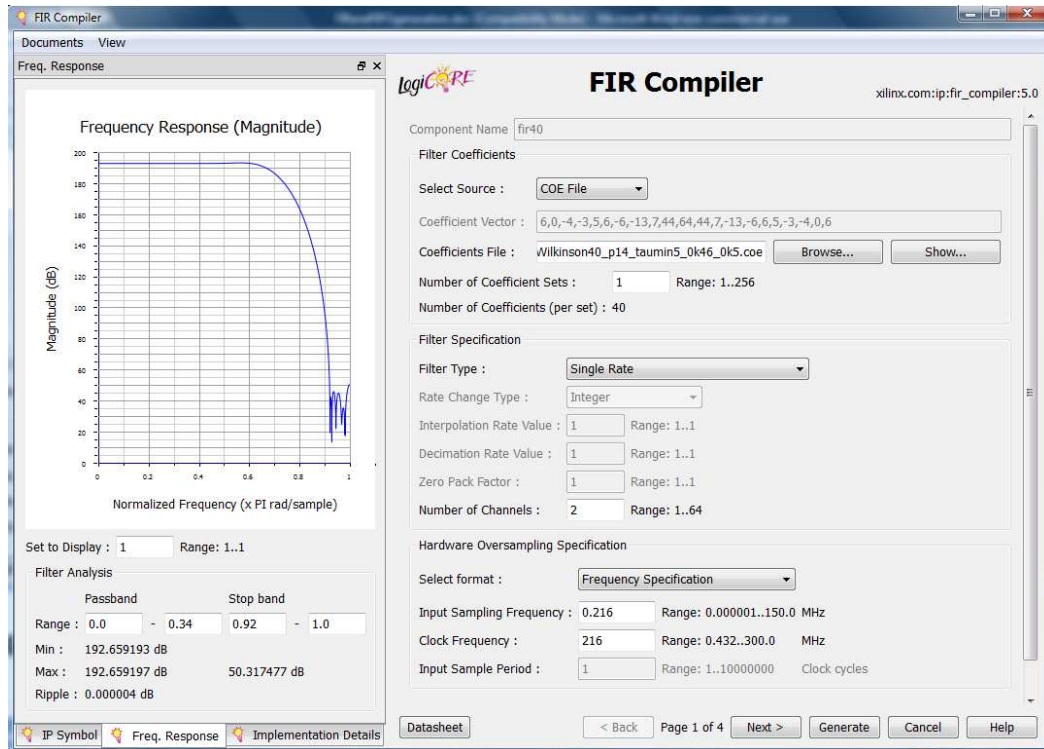
Coefficient file: IMPULSE_McClellan384_0k22675_0k25_2_1.coe

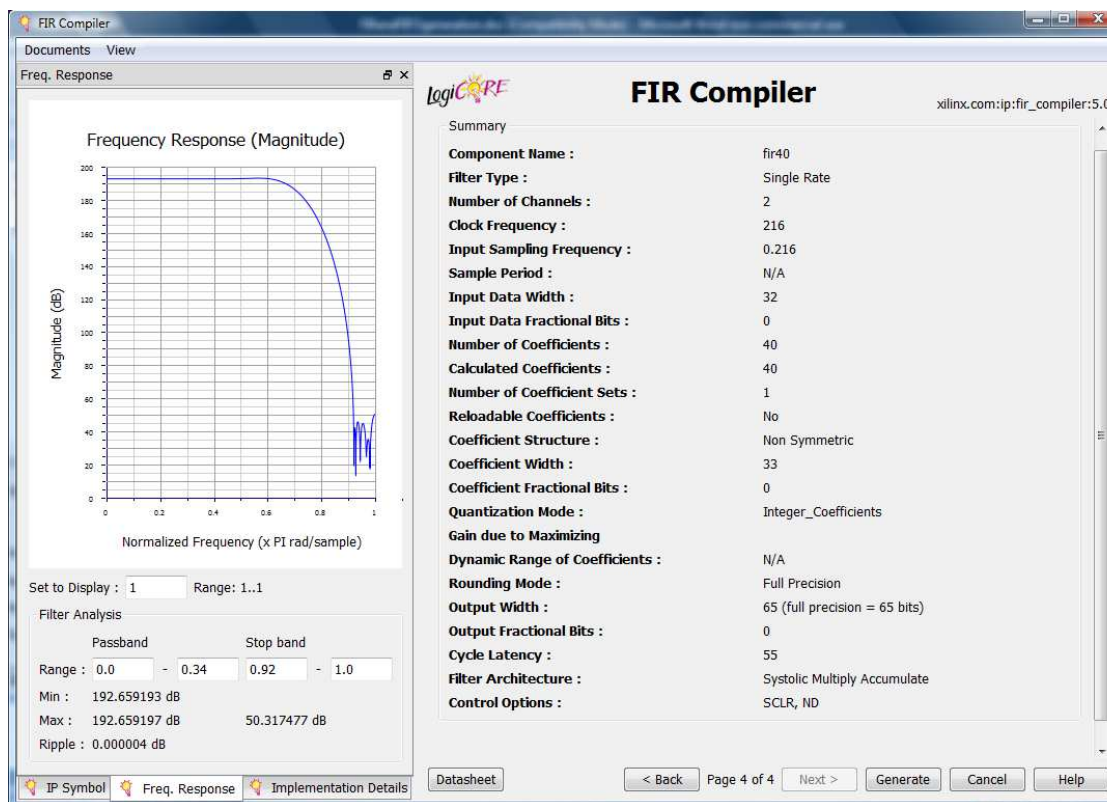
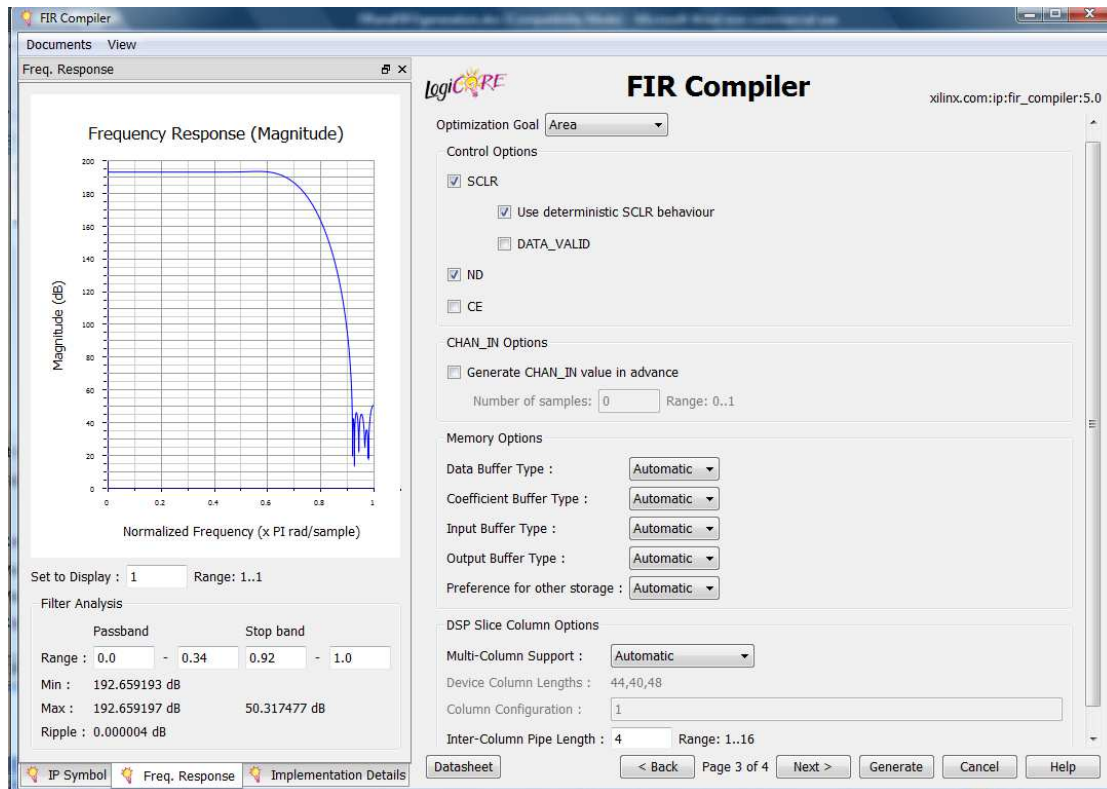




fir40, used in prefilter.v

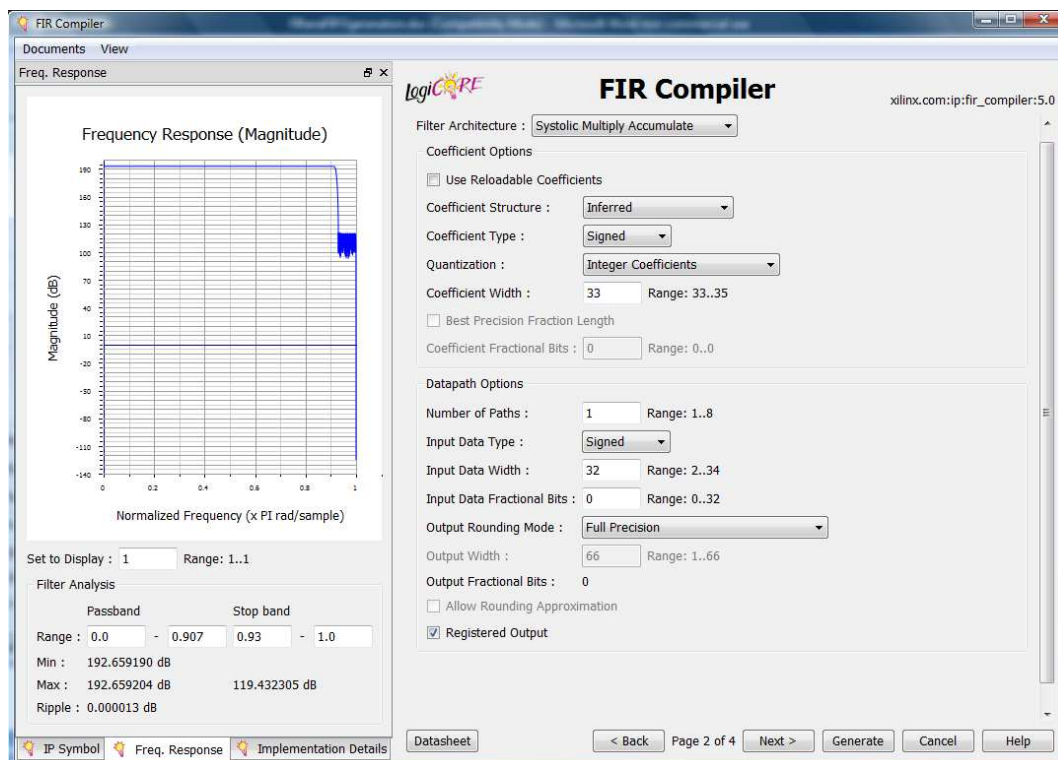
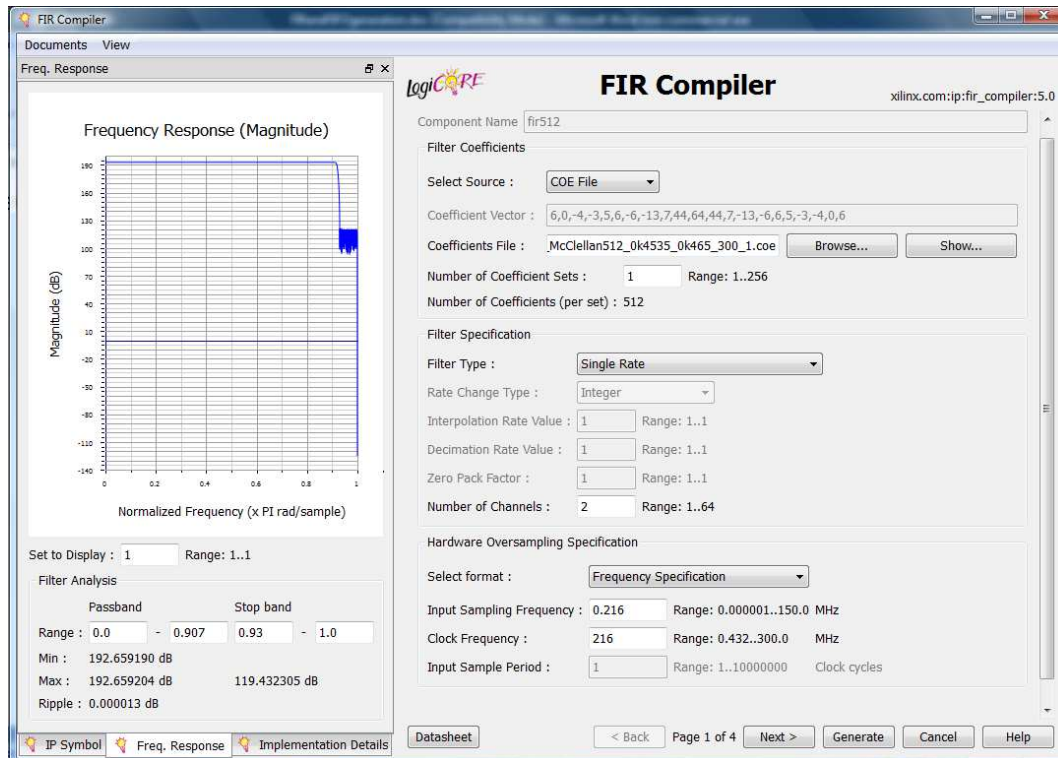
Coefficient file: IMPULSE_Wilkinson40_p14_taubin5_0k46_0k5.coe

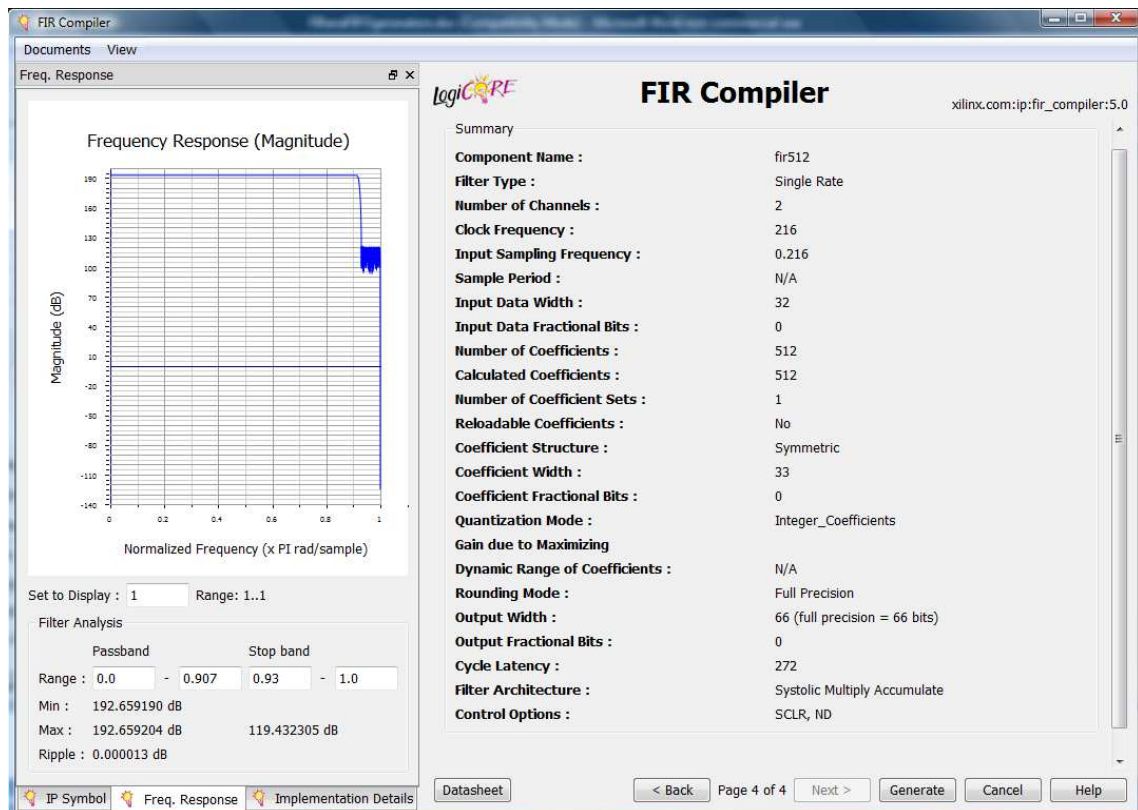
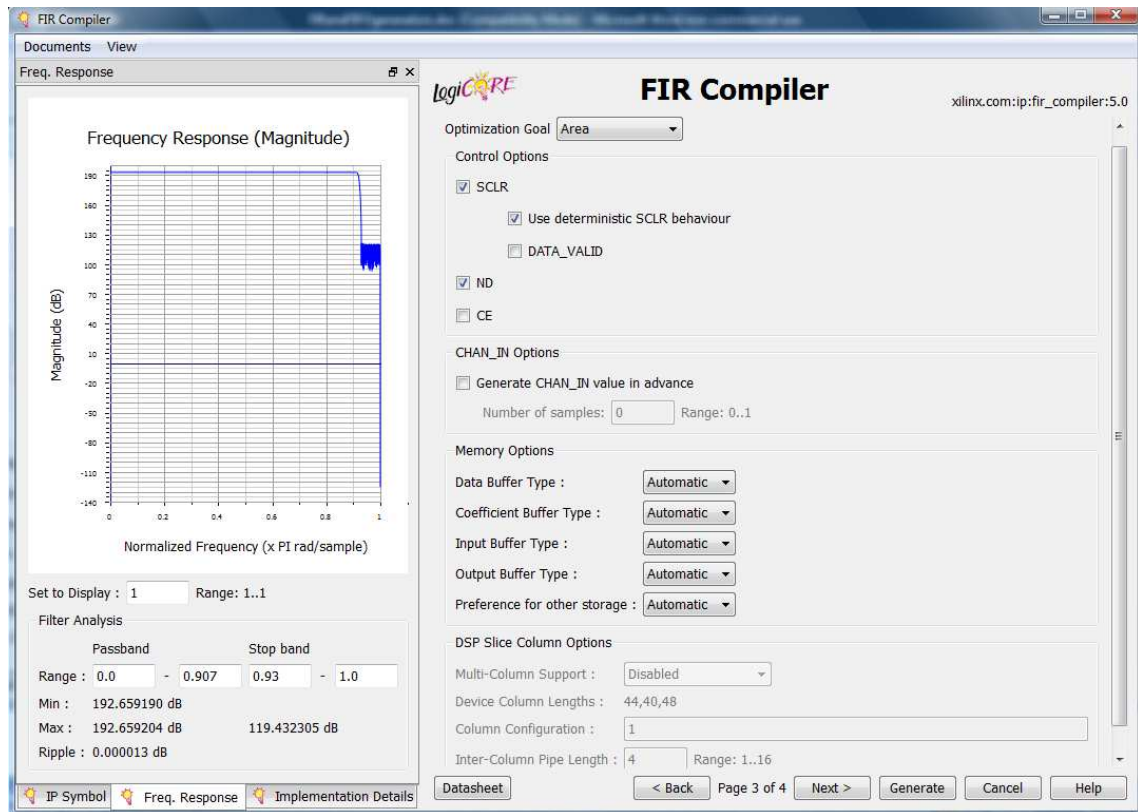




fir512, used in prefilter.v

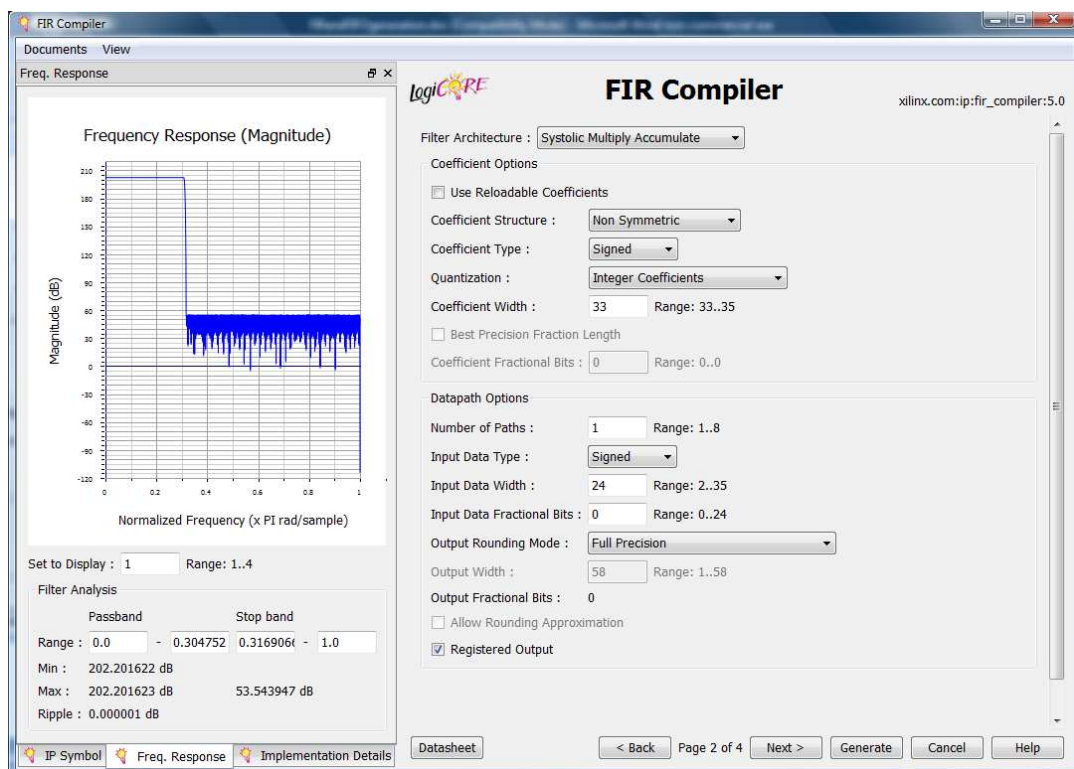
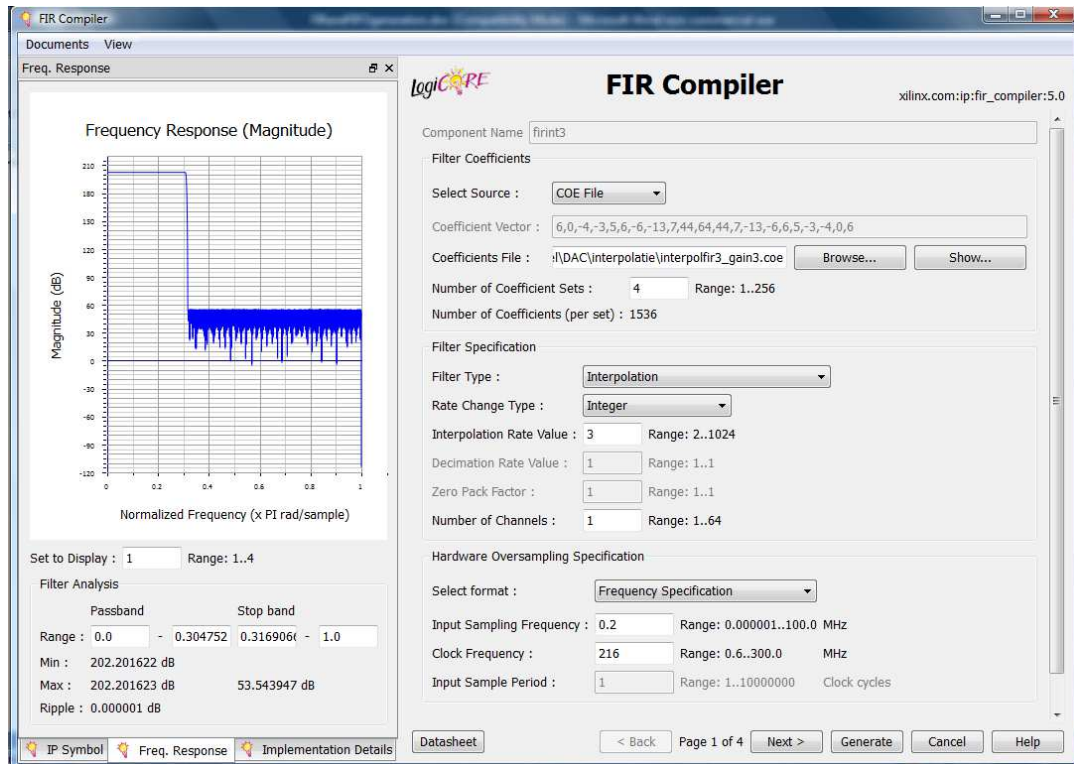
Coefficient file: IMPULSE_McClellan512_0k4535_0k465_300_1.coe

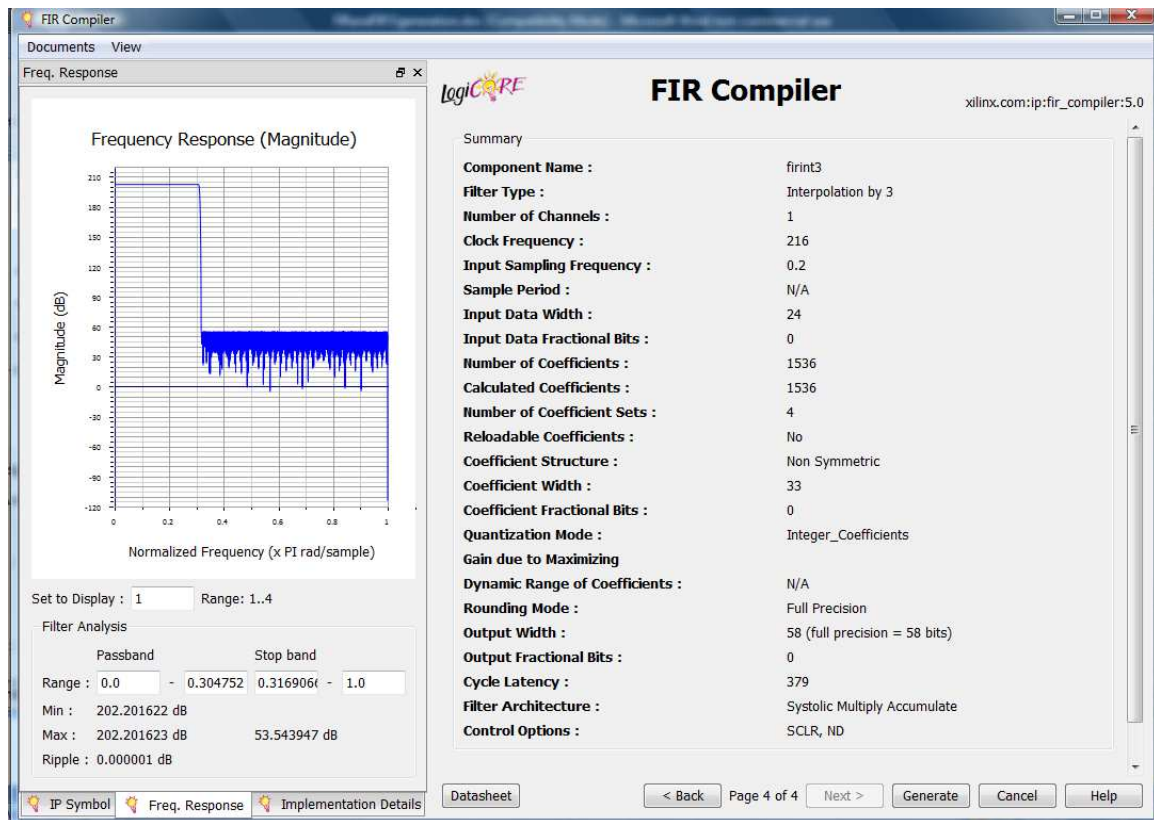
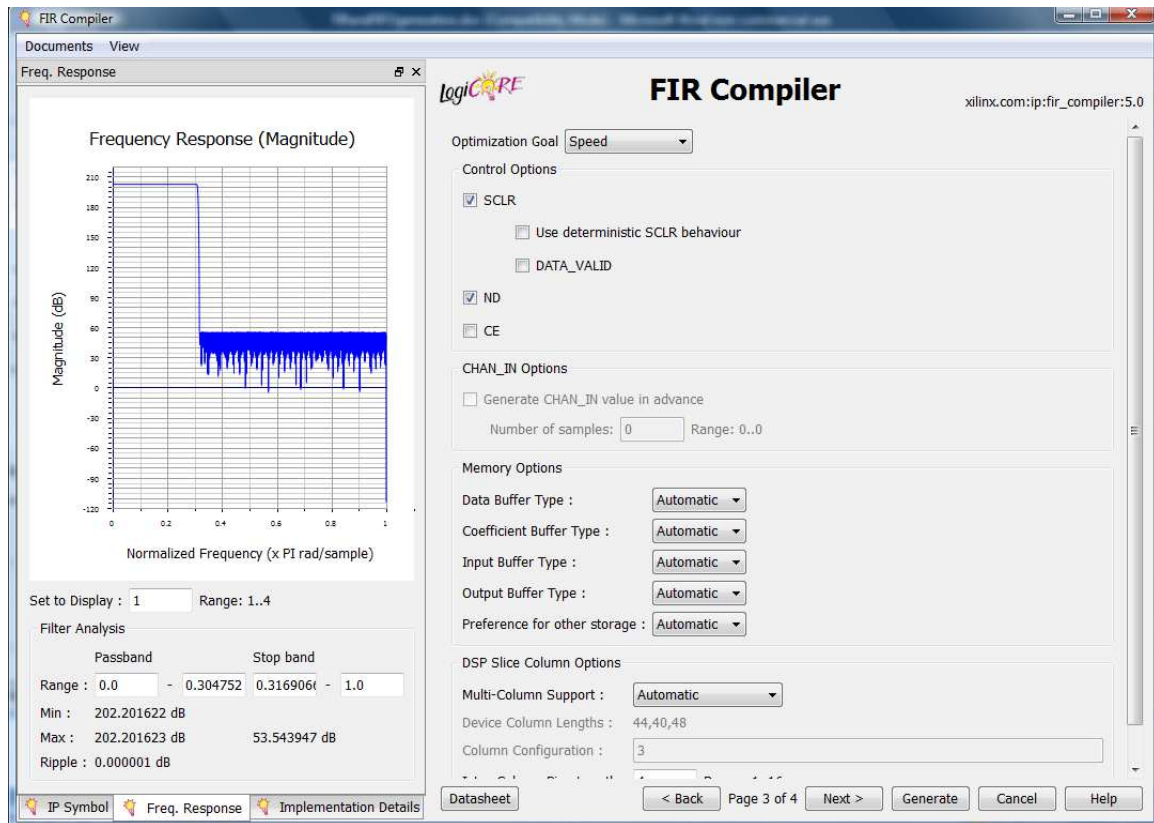




firint3, three times interpolating filter used in interpol.v

Coefficient file: interpolfir3_gain3.coe





firint5, five times interpolating filter used in interpol.v

Coefficient file: IMPULSE_McClellan80_0k0304752_0k1683093333_1_2_verst5.coe

